

Please replace the paragraph from page 4, line 18, through page 5, line 2 with the following paragraph:

B2
In the present embodiment, the analog blocks are arranged in an array on a single integrated circuit, or chip. The analog system architecture can be generally referred to as a programmable analog "system-on-a-chip" block. Such blocks can be used in those applications that typically require multiple chips that may be fabricated using different technologies. Implementation in embedded applications, including audio, wireless, handheld, data communications, Internet control, and industrial and consumer systems, is contemplated.

Please replace page 13, line 10 with the following:

B3
A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE

Please replace page 33, lines 13-14 with the following:

B4
PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM

Please replace the paragraph at page 46, lines 15-20 with the following paragraph:

B5
The preferred embodiment of the present invention, programming methodology and architecture for a programmable analog system, is thus described. While the present invention has been described in particular embodiments, it should be appreciated that the present invention should not be construed as limited by such embodiments, but rather construed according to the following claims.

IN THE ABSTRACT

Please replace lines 1-2 of the Abstract (page 54) with the following: